

Let's Get Edgy Challenges in Al Processing for Defense and Aerospace Applications

Devon Yablonski - Principal Product Manager for Sensor Processing

Embedded Tech Trends - January, 2020





















The Story of Cher Ami and the Lost Battalion





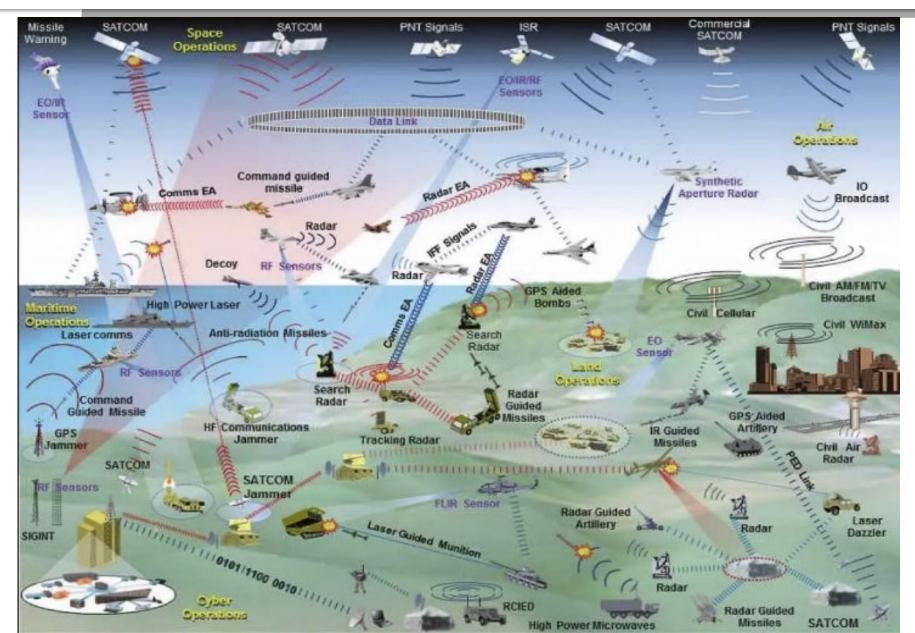


In battle, networks will be unreliable; Performance at the tactical edge is paramount



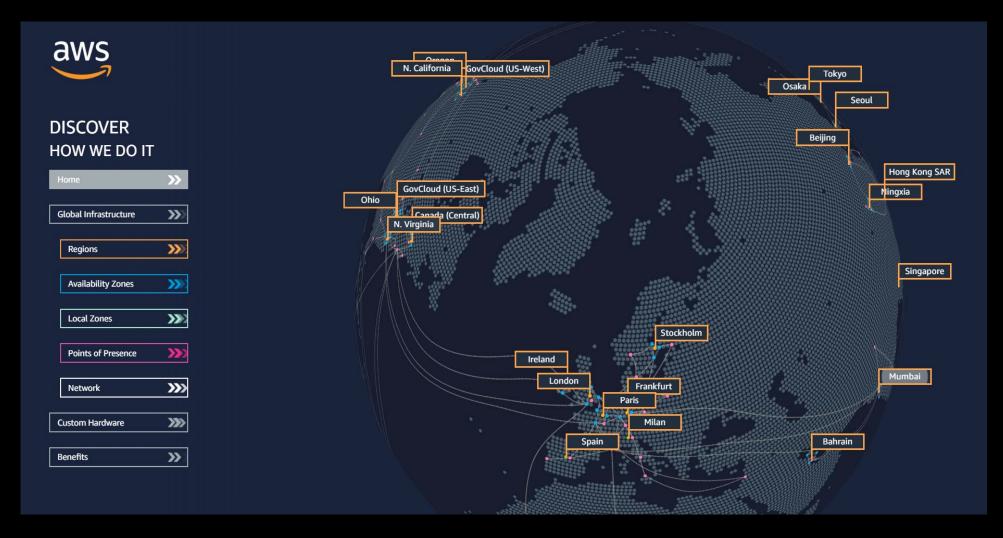
The Battlefield is a Big Data and Temporal Latency Problem







Artificial Intelligence Processing In the Commercial Environment...



... fails to translate to the tactical edge



Hardware that Delivers at the Tactical Edge



Common composable data center architecture from cloud to edge

















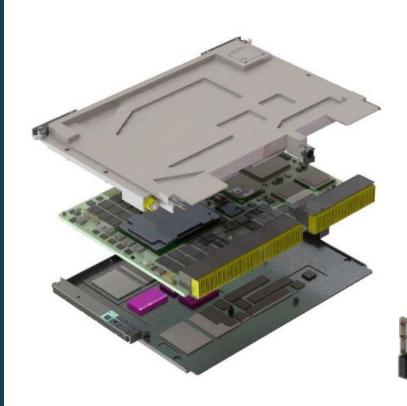




Anatomy of a Rugged, Secure Embedded Subsystem













Mercury Ecosystem Examples



Cascade Lake High Density Server HDS6605



- Xeon SP 1.5 TFLOPS via 22 cores
- 96/192/384 GB DDR4
- 100GbE/EDR IB data plane

PCI Express Switching SFM6126



- 10GbE Control Plane Switch
- PCle Gen3 Expansion Plane Switch
- 2x XMC Mezzanine Sites

I/O Module IOM-400



- Low Latency Wideband I/O
- 24 TX/RX or 12 Duplex 10Gb Fiber
- Rotated Fiber Optics for VITA 66



Composable OpenVPX AI Processing Capability



